Roll No ....

MCADD-201

M.C.A. (Dual Degree/Integrated Course), II Semester

Examination, May 2018

Computer Architecture

Time : Three Hours

Maximum Marks : 70

Note: i) Attempt any five questions.

ii) All questions carry equal marks.

1. a) Explain the Bus structure of computer system in detail. 7

   b) What is Instruction set? Explain the basic function of computer system. 7

2. a) Explain the design of Arthimetic and Logic Unit in detail. 7

   b) Difference between hardwired and micro programmed control unit. 7

3. a) Write a short note on Direct Memory Access (DMA). 7

   b) Explain the implementation of processor using the building blocks. 7

4. a) Differentiate between Synchronous and Asynchronous data transfer mode.   7

    b) Explain the instruction set of 8086 in detail.   7

5. a) What is Assembly language? Write any simple Assembly language program of 8086.   7

    b) Explain the concept of Virtual memory organization.   7

6. a) Difference between static RAM and dynamic RAM.   7

    b) Explain the addressing mode of 8086 microprocessor.   7

7. a) Explain the execution of instruction in terms of microinstruction in detail.   7

    b) Explain the Architecture of 8086 microprocessor.   7

8. Write a short notes on any two: 14

    a) Segmentation

    b) Paging

    c) Cache memory

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